

## Technical data: Technology CPUs

Technology CPU	CPU 315T-3 PN/DP	CPU 317T-3 PN/DP	CPU 317TF-3 PN/DP
Dimensions	120 x 125 x 130		
Required front connector	1 x 40-pin		
Order No. group:			
- CPU 6ES7	315-7TJ.	317-7TK.	317-7UL.
- S7-Technology 6ES7	864-1CC.		
<b>Memory</b>			
Work memory	384 KB	1 MB	1.5 MB
Instructions	84 K	333 K	400 K
<b>Processing times</b>			
Bit operation	0.05 µs	0.025 µs	
Word operation	0.09 µs	0.03 µs	
Fixed-point operation	0.12 µs	0.04 µs	
Floating-point operation	0.45 µs	0.16 µs	
<b>Bit memories/timers/counters</b>			
Bit memory	4 096 bytes		
S7 timers/S7 counters	256 / 256	512 / 512	
IEC timers/IEC counters	● *)		
<b>Address ranges</b>			
I/O address area	2 048 / 2 048 bytes	8 192 / 8 192 bytes	
I/O process image	2 048 / 2 048 bytes		
Digital channels (central)	512		
Analog channels (central)	64		
<b>DP interfaces</b>			
DP master systems internal / CP 342-5	● / ●		
DP slave	●		
<b>PROFINET interface</b>			
PROFINET IO	●		
PROFINET with IRT	●		
<b>Open User Communication (OUC)</b>			
TCP/IP	●		
UDP	●		
ISO-on-TCP (RFC 1006)	●		
Webserver	●		
<b>Integrated inputs/outputs</b>			
Digital inputs	4 x 24 V DC; for BERO evaluation, for example		
Digital outputs	8 x 24 V DC, 0.5 A: for high-speed cam switching functions		
<b>Integrated functions</b>			
	Gearbox synchronism and curve synchronism		
	Traversing to fixed stop		
	Registration mark correction via measuring probe		
	Path- or time-dependent cam switching		
	Controlled positioning		
Fail-safety	●		
<b>Maximum quantity structure for technology</b>			
Axes	8	32	
Cam disks	16	32	
Cams	16	32	
Measuring probes	8	16	
External encoders	8	16	
Used simultaneously	32	64	

\*) Via SFB, number unlimited or limited only by main memory